

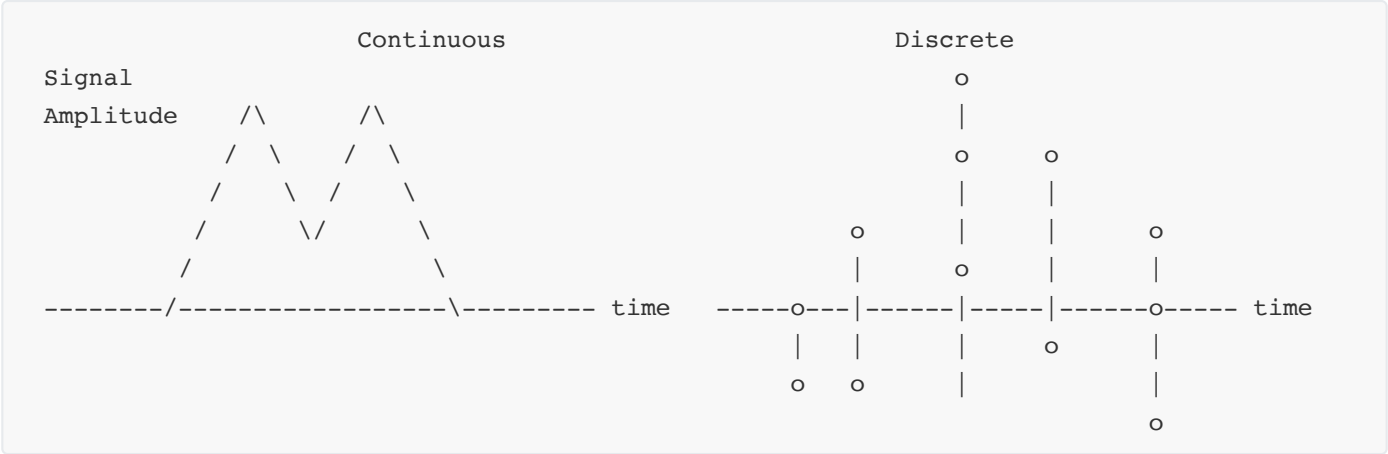
Question 1(a) [3 marks]

Define Continuous time Signal and Discrete time Signal with Wave form.

Answer:

Signal Type	Definition	Waveform
Continuous Time Signal	Signal defined for all values of time with no breaks	<pre>mermaid graph LR; A[t] --> B[x(t)]; style B fill:#fff,stroke:#333,stroke-width:2px</pre>
Discrete Time Signal	Signal defined only at discrete time intervals	<pre>mermaid graph LR; A[n] --> B[x[n]]; style B fill:#fff,stroke:#333,stroke-width:2px</pre>

Diagram:



Mnemonic: "Continuous Curves, Discrete Dots"

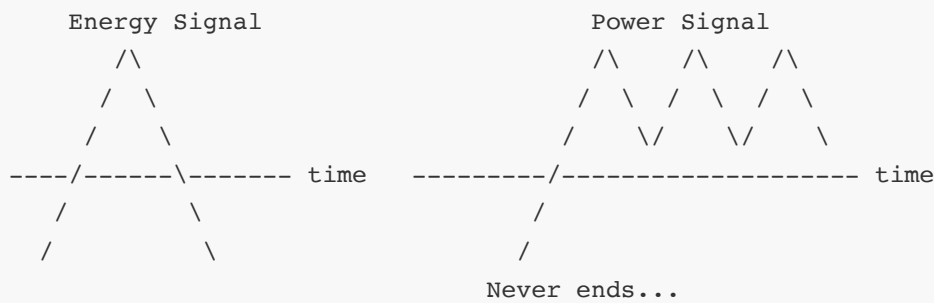
Question 1(b) [4 marks]

Explain Energy and power signal.

Answer:

Parameter	Energy Signal	Power Signal
Definition	Has finite energy but zero average power	Has finite average power but infinite energy
Mathematical Expression	$\int x(t) ^2 dt < \infty$	$\lim_{T \rightarrow \infty} (1/2T) \int x(t) ^2 dt < \infty$
Examples	Pulse, Decaying exponential	Sine wave, Square wave
Nature	Finite duration or decreasing amplitude	Periodic or infinite duration

Diagram:



Mnemonic: "Energy Expires, Power Persists"

Question 1(c) [7 marks]

Explain block diagram of digital communication system.

Answer:



Block	Function
Source	Generates message to be transmitted
Source Encoder	Converts message to digital sequence, removes redundancy
Channel Encoder	Adds controlled redundancy for error detection/correction
Digital Modulator	Converts digital symbols to waveforms suitable for channel
Channel	Transmission medium, adds noise and distortion
Digital Demodulator	Converts received waveforms back to digital symbols
Channel Decoder	Detects/corrects errors using added redundancy
Source Decoder	Reconstructs original message from digital sequence

Mnemonic: "Send Signals Carefully, Digital Messages Communicate Data Safely"

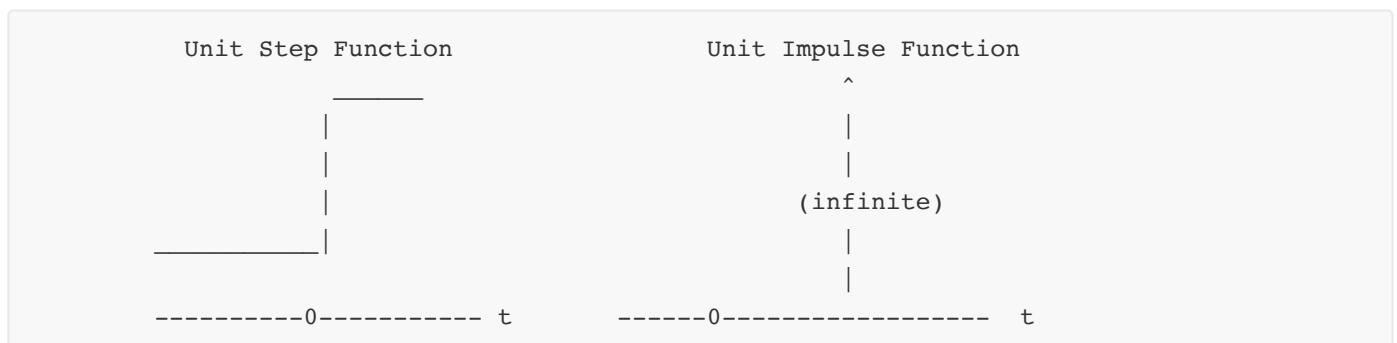
Question 1(c) OR [7 marks]

Explain Unit Step function and Unit impulse function.

Answer:

Function	Mathematical Definition	Properties	Applications
Unit Step Function ($u(t)$)	$u(t) = 0$ for $t < 0$ $u(t) = 1$ for $t \geq 0$	- Represents sudden transition - Integral of impulse function	System response analysis
Unit Impulse Function ($\delta(t)$)	$\delta(t) = 0$ for $t \neq 0$ $\int \delta(t) dt = 1$	- Infinitesimally narrow pulse - Sampling property - Derivative of step function	Sampling, system analysis

Diagrams:



Mnemonic: "Step Stays steady after zero, Impulse Instantly appears then vanishes"

Question 2(a) [3 marks]

A signal carries 8 bit/signal elements. If 1000 signal elements sent per second. Find the bit rate.

Answer:

Parameter	Value
Bits per signal element	8 bits
Signal elements per second	1000
Calculation	Bit rate = (Bits per signal element) \times (Signal elements per second)
Bit rate	$= 8 \times 1000 = 8000$ bits/second or 8 kbps

Mnemonic: "Bits per signal \times Signals per second = Bits per second"

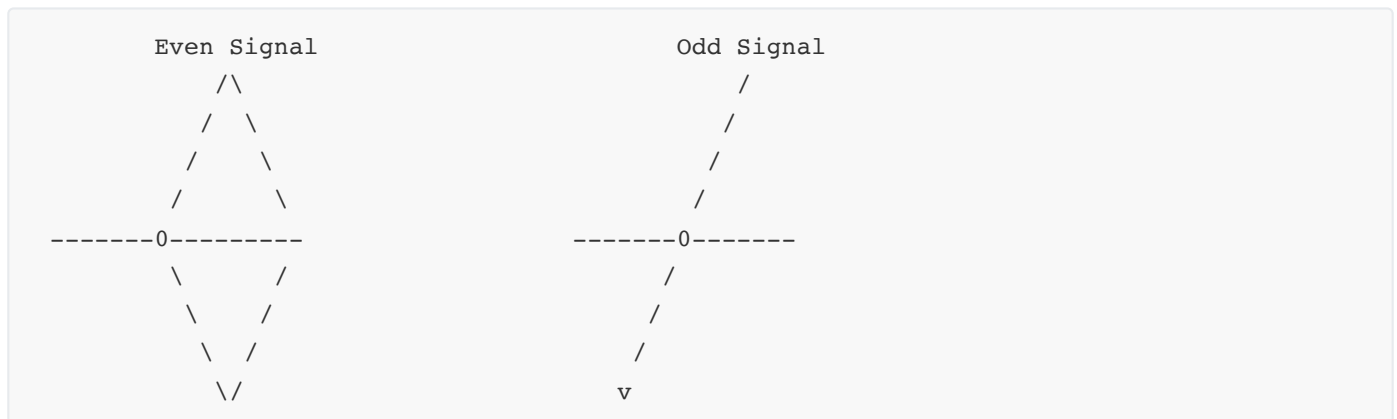
Question 2(b) [4 marks]

Explain Even and Odd signal.

Answer:

Signal Type	Mathematical Definition	Properties	Examples
Even Signal	$x(-t) = x(t)$	<ul style="list-style-type: none"> - Symmetric about y-axis - Cosine is even 	Cosine function, $ t $
Odd Signal	$x(-t) = -x(t)$	<ul style="list-style-type: none"> - Anti-symmetric about y-axis - Sine is odd 	Sine function, t

Diagram:



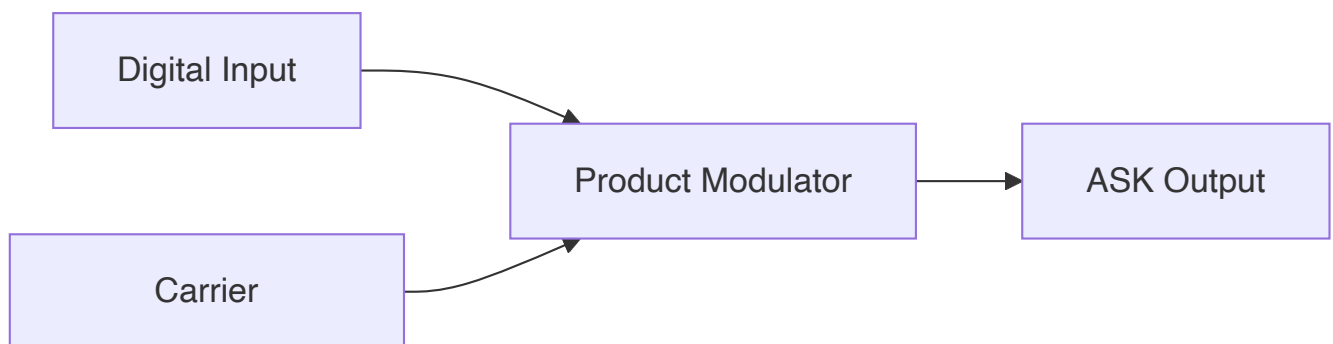
Mnemonic: "Even reflects Exactly, Odd reflects Oppositely"

Question 2(c) [7 marks]

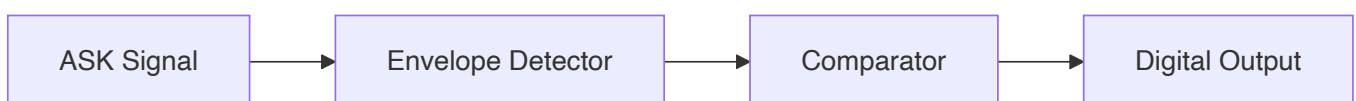
Explain the block diagram of ASK modulator and de-modulator with waveform.

Answer:

ASK Modulator:



ASK Demodulator:

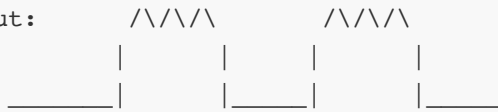


Waveforms:

Digital Input:

[illegible]

ASK Output:



Concept	Description
ASK Modulation	Amplitude varies according to digital data (0 or 1)
Modulator Components	Product modulator multiplies carrier with digital signal
Demodulator Components	Envelope detector extracts amplitude, comparator regenerates digital signal

Mnemonic: "ASK Adjusts Signal's Knockout amplitude"

Question 2(a) OR [3 marks]

A signal has a bit rate of 4000 bit/second and a baud rate of 1000 baud. How many data elements are carried by each signal element?

Answer:

Parameter	Value
Bit rate	4000 bits/second
Baud rate	1000 baud (signal elements/second)
Formula	Number of data elements = Bit rate ÷ Baud rate
Data elements per signal	= 4000 ÷ 1000 = 4 bits/signal element

Mnemonic: "Bits divided by Bauds equals Bits per signal"

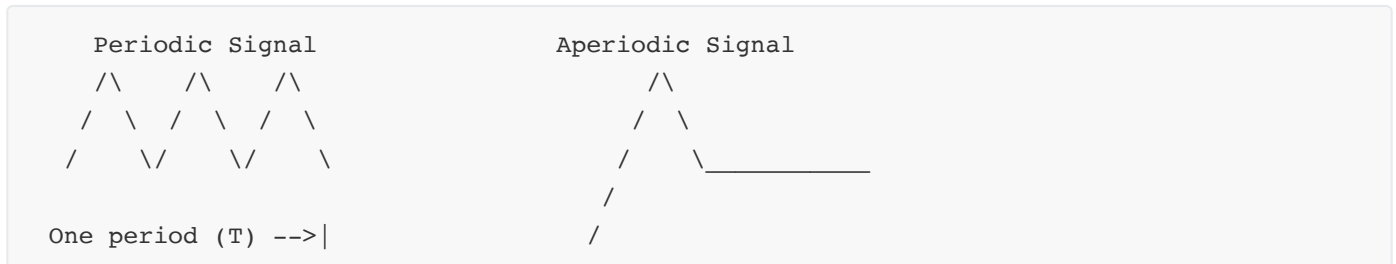
Question 2(b) OR [4 marks]

Explain Periodic and aperiodic signal.

Answer:

Signal Type	Definition	Mathematical Condition	Examples
Periodic Signal	Repeats after fixed time interval	$x(t) = x(t+T)$ for all t	Sine wave, Square wave
Aperiodic Signal	Does not repeat after any time interval	$x(t) \neq x(t+T)$ for any T	Pulse, Noise

Diagram:



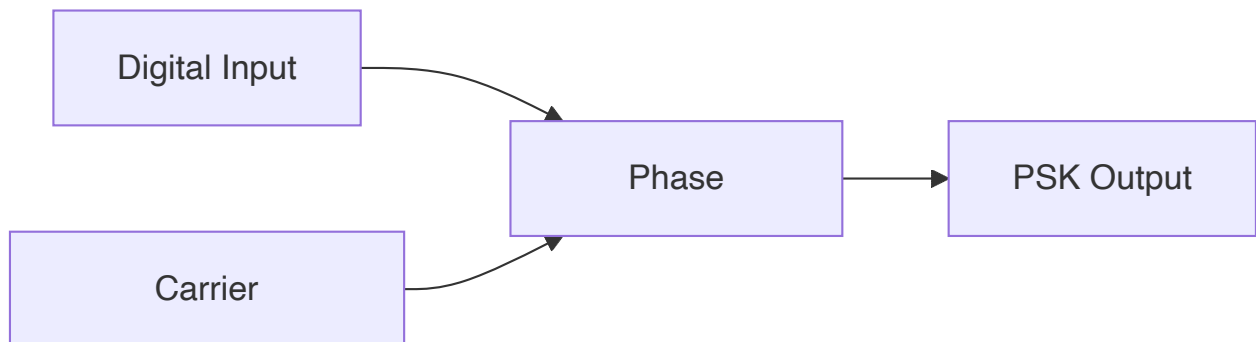
Mnemonic: "Periodic Perfectly repeats, Aperiodic Alters always"

Question 2(c) OR [7 marks]

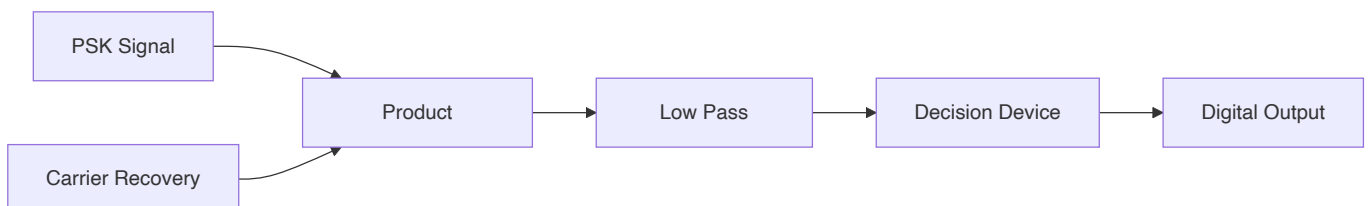
Explain the block diagram of PSK modulator and de-modulator with waveform.

Answer:

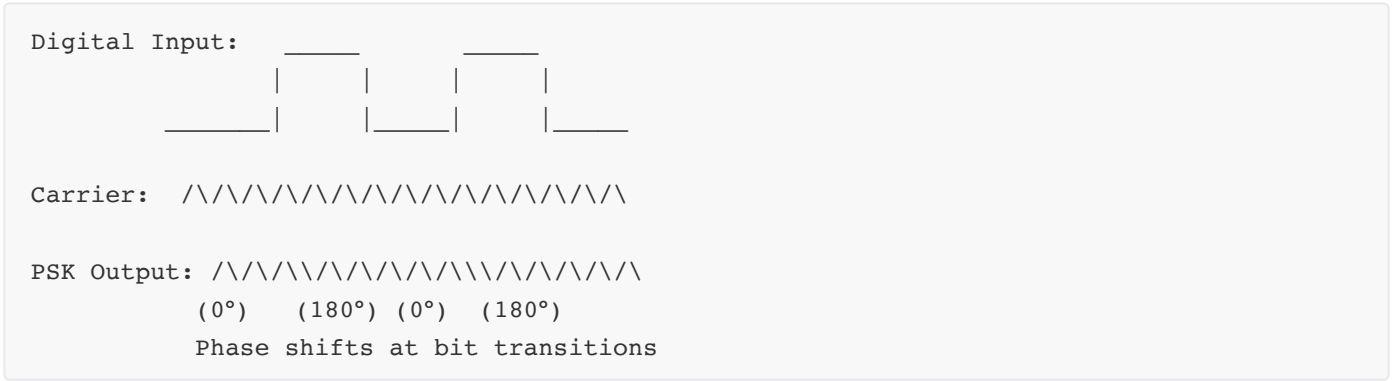
PSK Modulator:



PSK Demodulator:



Waveforms:



Parameter	Description
PSK Modulation	Phase shifts according to digital data (0 or 1)
Phase States	0° for bit '1', 180° for bit '0'
Advantages	Better noise immunity than ASK

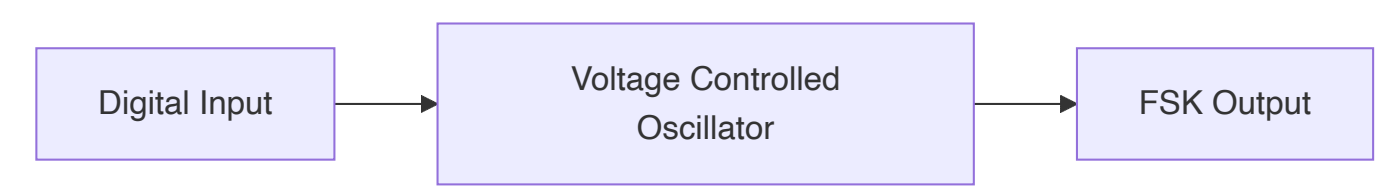
Mnemonic: "PSK Phases Shift with Knowledge"

Question 3(a) [3 marks]

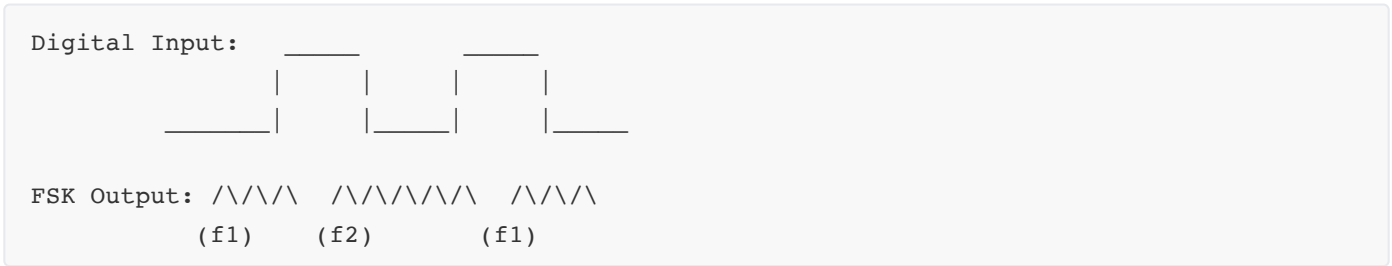
Explain the working of FSK modulator with block diagram and output Waveform.

Answer:

FSK Modulator Block Diagram:



FSK Waveforms:



- **Principle:** Digital bit '1' sends carrier with frequency f1, bit '0' sends carrier with frequency f2
- **Working:** Voltage controlled oscillator changes frequency based on input bit value

Mnemonic: "Frequency Shifts for Knowledge transmission"

Question 3(b) [4 marks]

Draw the PSK modulation waveform for the sequence of 1010110110.

Answer:

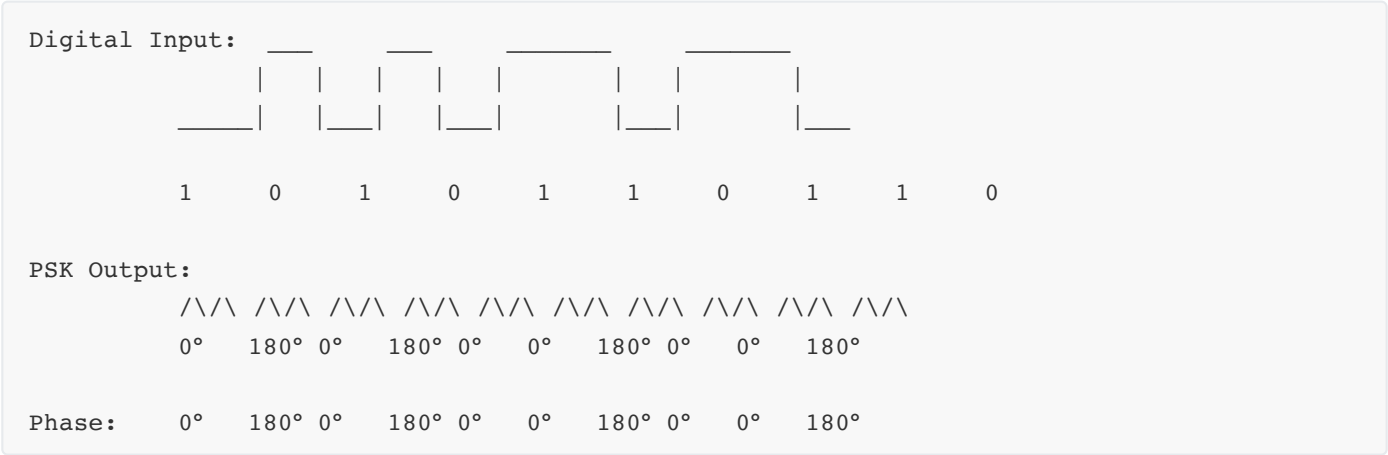


Table for PSK modulation:

Bit	Phase
1	0°
0	180°

Mnemonic: "One-Zero, Phase-Shifts, Keep-Signal Modulated"

Question 3(c) [7 marks]

Draw the ASK and FSK modulation waveform for the sequence of 1100110101.

Answer:

Digital Input Sequence: 1100110101

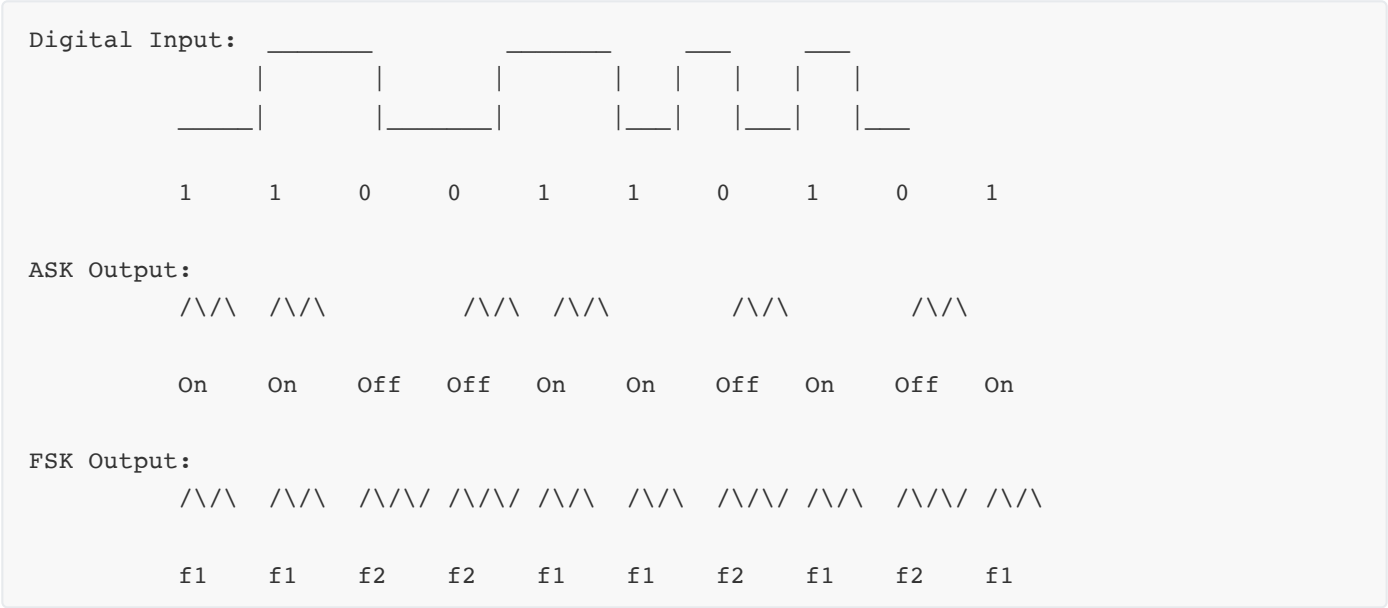


Table for comparison:

Bit	ASK	FSK
1	Carrier ON (high amplitude)	Higher frequency (f1)
0	Carrier OFF (zero/low amplitude)	Lower frequency (f2)

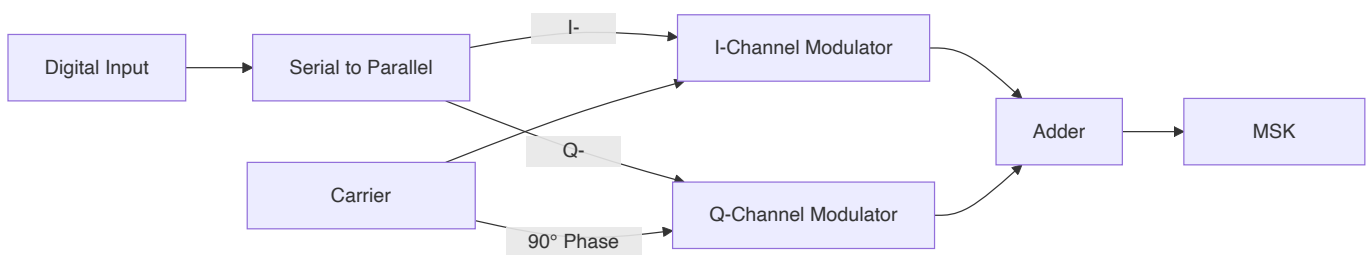
Mnemonic: "Amplitude Shows Knowledge, Frequency Shifts Knowledge"

Question 3(a) OR [3 marks]

Explain the working of MSK modulator with block diagram and output Waveform.

Answer:

MSK Modulator Block Diagram:



MSK Features:

- Continuous phase FSK with frequency deviation exactly half bit rate
- Phase changes occur smoothly (no abrupt phase changes)
- Better spectral efficiency than FSK

Mnemonic: "Minimum Shift Keeps spectrum narrow"

Question 3(b) [4 marks]

Draw the constellation diagram of 8-PSK and 16-QAM.

Answer:

8-PSK Constellation:

```

001 *   *   000
    /|\  /|\
    |   |
010 * |   | * 111
    \ |   | /
    \ |   | /
011 *   *   110
    /|\  /|\
    / \  /\
100 *   \ / * 101
  
```

16-QAM Constellation:

*	*	*	*
0000	0001	0100	0101
*	*	*	*
0010	0011	0110	0111
*	*	*	*
1000	1001	1100	1101
*	*	*	*
1010	1011	1110	1111

Modulation	Description
8-PSK	8 points equally spaced around circle, 3 bits per symbol
16-QAM	16 points in square grid, varying amplitude and phase, 4 bits per symbol

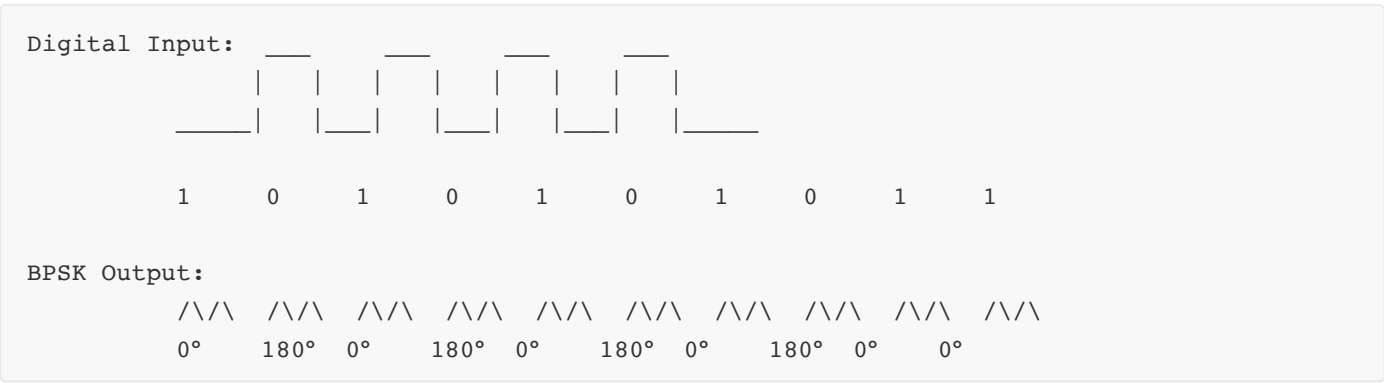
Mnemonic: "PSK Points on Single circle, QAM Quadrature Amplitude Matrix"

Question 3(c) OR [7 marks]

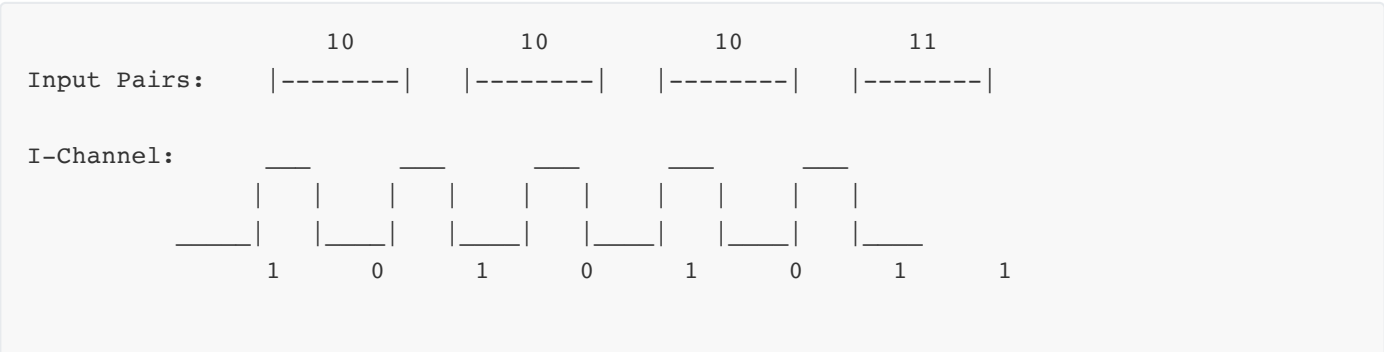
Draw BPSK and QPSK modulation waveform for 1010101011.

Answer:

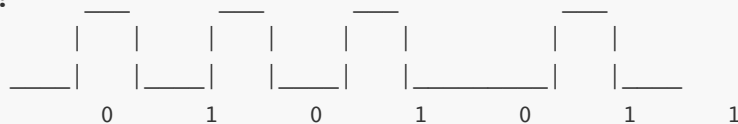
BPSK Modulation:



QPSK Modulation (grouping bits in pairs):



Q-Channel:



QPSK Phase: 90° 270° 90° 270° 90° 270° 90° 45°

Bit Pair	QPSK Phase
10	90°
00	180°
01	270°
11	0°

Mnemonic: "Binary Phase Shifts Keys, Quadrature Phase Shifts Keys"

Question 4(a) [3 marks]

Encode the data using Shanon Fano code for below probability sequence. $P = \{0.30, 0.25, 0.20, 0.12, 0.08, 0.05\}$

Answer:

Symbol	Probability	Shannon-Fano Code
S1	0.30	00
S2	0.25	01
S3	0.20	10
S4	0.12	110
S5	0.08	1110
S6	0.05	1111

Process:

- Sort symbols by decreasing probability
- Split into two groups with nearly equal probability ($0.30+0.25=0.55$, $0.20+0.12+0.08+0.05=0.45$)
- Assign 0 to first group, 1 to second group
- Recursively continue for each subgroup

Mnemonic: "Separate, Fano divides, Code efficiently"

Question 4(b) [4 marks]

Explain Hamming code.

Answer:

Aspect	Description
Definition	Linear error-correcting code that detects double errors and corrects single errors
Parity bits	For m data bits, need k parity bits where $2^k \geq m+k+1$
Position	Parity bits placed at positions 1, 2, 4, 8, 16... (powers of 2)
Error detection	Calculate syndrome to locate error position

Example Hamming(7,4):

Positions: 1 2 3 4 5 6 7

 P1 P2 D1 P4 D2 D3 D4

Parity check equations:

P1 checks: P1, D1, D2, D4

P2 checks: P2, D1, D3, D4

P4 checks: P4, D2, D3, D4

Mnemonic: "Hamming Helps Handle Bit Errors"

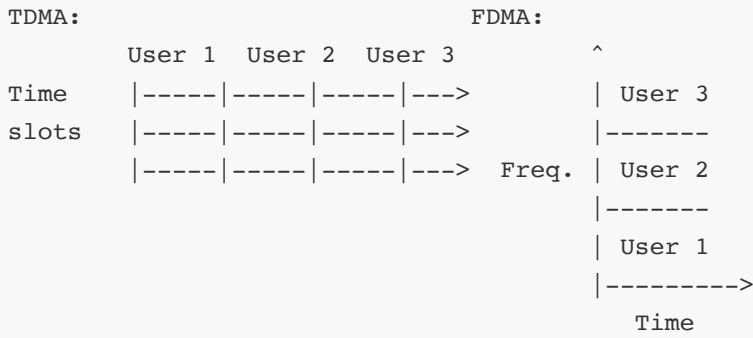
Question 4(c) [7 marks]

Compare TDMA and FDMA.

Answer:

Parameter	TDMA (Time Division Multiple Access)	FDMA (Frequency Division Multiple Access)
Basic Principle	Divides channel by time slots	Divides channel by frequency bands
Resource Allocation	Each user gets entire bandwidth for short time	Each user gets portion of bandwidth all the time
Guard Period	Time guard bands between slots	Frequency guard bands between channels
Synchronization	Strict timing synchronization required	No timing synchronization needed
Efficiency	Higher, due to burst transmission	Lower, due to fixed assignment
Complexity	More complex	Relatively simple
Examples	GSM, DECT	FM radio, Traditional satellite systems

Diagram:



Mnemonic: "Time Divides Multiple Access, Frequency Divides Multiple Access"

Question 4(a) OR [3 marks]

Encode the data using Huffman code for below probability sequence. $P = \{0.4, 0.2, 0.2, 0.1, 0.1\}$

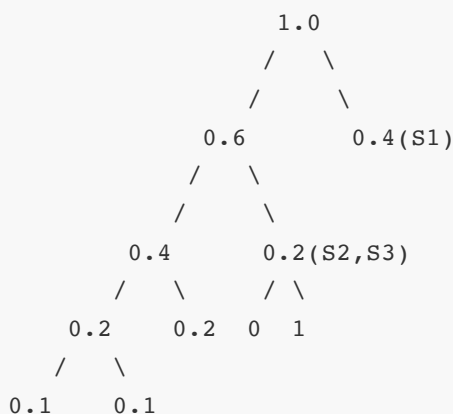
Answer:

Symbol	Probability	Huffman Code
S1	0.4	0
S2	0.2	10
S3	0.2	11
S4	0.1	100
S5	0.1	101

Process:

1. Start with sorted probabilities
2. Combine lowest two probabilities ($0.1+0.1=0.2$)
3. Rearrange and repeat until only two nodes remain
4. Assign bits by traversing tree

Tree Construction:



Mnemonic: "Huffman Helps encode High-frequency data"

Question 4(b) OR [4 marks]

Explain parity code.

Answer:

Aspect	Description
Definition	Simple error detection scheme that adds parity bit
Types	Even parity: total 1s is even Odd parity: total 1s is odd
Calculation	XOR all data bits to generate parity bit
Capability	Detects odd number of errors, cannot correct errors

Examples:

Even Parity:

Data: 1011 → Parity: 0 → Coded: 10110 (Even number of 1s: 4)

Odd Parity:

Data: 1011 → Parity: 1 → Coded: 10111 (Odd number of 1s: 5)

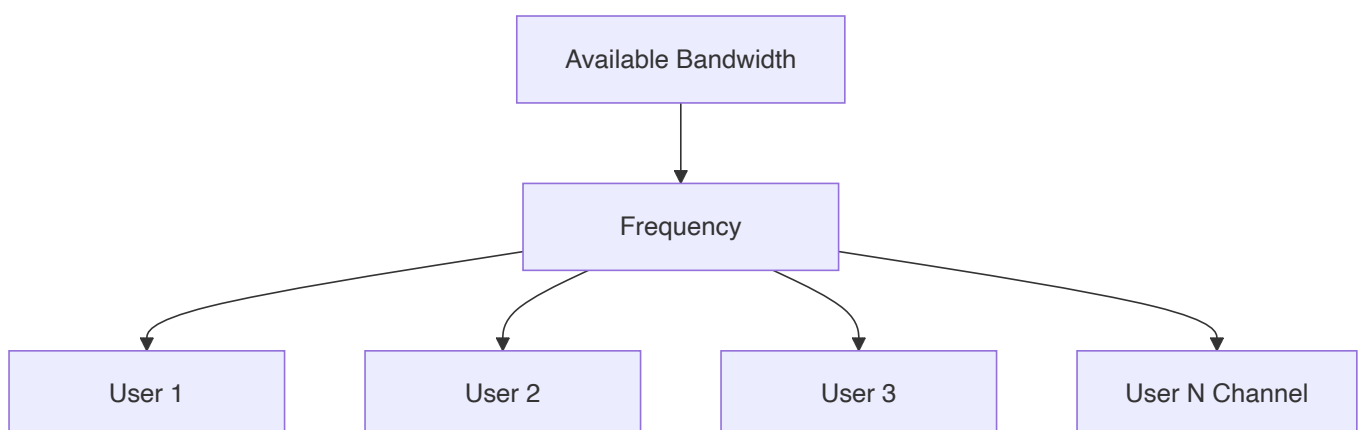
Mnemonic: "Parity Provides Primitive Error detection"

Question 4(c) OR [7 marks]

Explain FDMA Technique in detail.

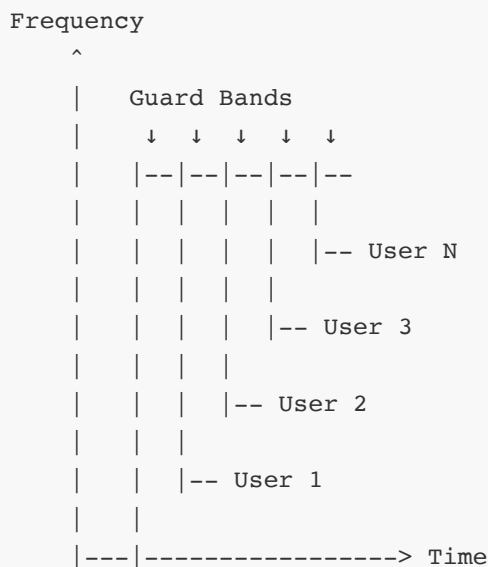
Answer:

FDMA (Frequency Division Multiple Access):



Parameter	Description
Basic Principle	Total bandwidth divided into non-overlapping frequency bands
Channel Assignment	Each user assigned dedicated frequency band
Guard Bands	Small frequency gaps between channels to prevent interference
Duplexing	Usually implemented with FDD (Frequency Division Duplexing)
Advantages	Simple implementation, no synchronization required
Disadvantages	Inefficient for bursty traffic, fixed allocation wastes bandwidth
Applications	AM/FM radio, Traditional cable TV, First-generation mobile systems

Frequency Allocation:



Mnemonic: "Fixed Division for Multiple Access"

Question 5(a) [3 marks]

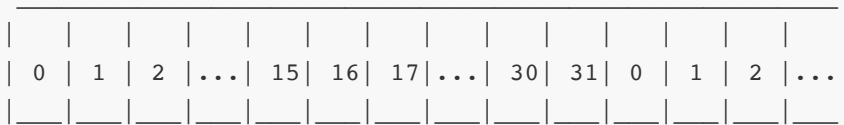
Explain E1 Career system.

Answer:

Parameter	Description
Description	European standard digital transmission format
Capacity	2.048 Mbps
Channel Structure	32 time slots (numbered 0-31)
Voice Channels	30 voice channels (64 kbps each)
Signaling	Time slot 16 for signaling
Frame Alignment	Time slot 0 for synchronization

Diagram:

One E1 Frame (32 time slots)



TS0: Frame alignment

TS16: Signaling

TS1-15, TS17-31: Voice/data channels (30 channels)

Mnemonic: "E1 Enables 30 + 2 time slots"

Question 5(b) [4 marks]

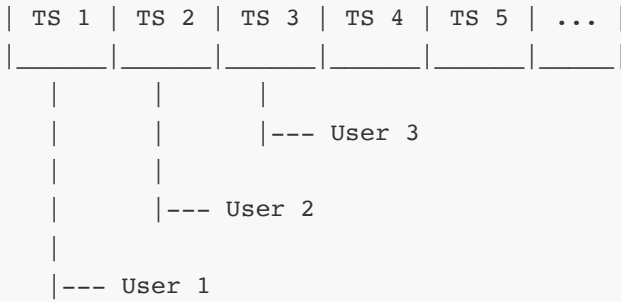
Explain TDMA Access technique.

Answer:

Parameter	Description
Definition	Multiple access technique that divides time into slots for different users
Working Principle	Each user gets entire bandwidth for a short time period
Frame Structure	Time divided into frames, frames divided into slots
Guard Time	Small time gap between slots to prevent overlap
Synchronization	Requires precise timing synchronization

TDMA Frame Structure:





Each time slot (TS) contains:

- User data
- Guard time
- Synchronization bits
- Control bits

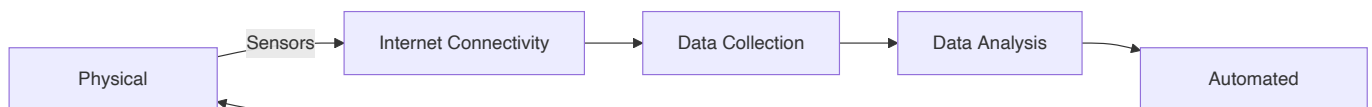
Mnemonic: "Time Divides Multiple Access"

Question 5(c) [7 marks]

Explain IoT – Concept, Features, Advantages and Disadvantages.

Answer:

IoT Concept:



Aspect	Description
Concept	Network of physical objects embedded with sensors, software, and connectivity
Features	<ul style="list-style-type: none"> - Connectivity (devices connected to internet) - Intelligence (smart decision making) - Sensing (collecting data from environment) - Automation (minimal human intervention) - Scalability (handles many devices)
Advantages	<ul style="list-style-type: none"> - Improved efficiency and productivity - Better resource management - Enhanced decision making - Convenience and time-saving - New business opportunities
Disadvantages	<ul style="list-style-type: none"> - Security vulnerabilities - Privacy concerns - Complexity in implementation - Compatibility issues - Dependence on internet

Application Areas:

- Smart homes, cities
- Healthcare monitoring
- Industrial automation
- Agriculture
- Transportation

Mnemonic: "Internet of Things: Connected, Automated, Smarter Decisions"

Question 5(a) OR [4 marks]

Explain T1 Career TDM system.

Answer:

Parameter	Description
Description	North American standard digital transmission format
Capacity	1.544 Mbps
Channel Structure	24 time slots (channels) + 1 framing bit
Voice Channels	24 voice channels (64 kbps each)
Frame Structure	193 bits per frame ($24 \times 8 + 1$)
Signaling	Robbed bit signaling (least significant bit)

Diagram:

One T1 Frame (193 bits)



F: Framing bit

Each channel: 8 bits (1 byte)

Mnemonic: "T1 Transmits 24 channels"

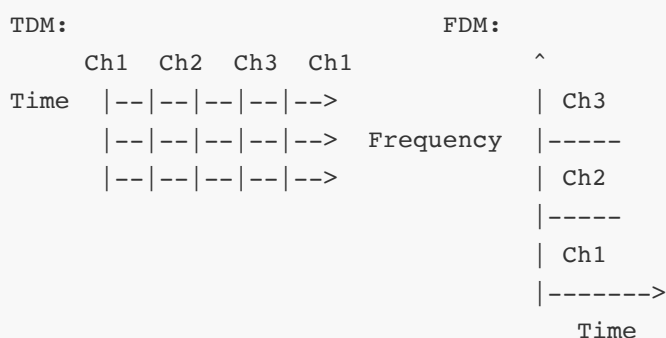
Question 5(b) OR [3 marks]

Compare TDM and FDM.

Answer:

Parameter	TDM (Time Division Multiplexing)	FDM (Frequency Division Multiplexing)
Basic Principle	Divides channel by time	Divides channel by frequency
Signal Separation	In time domain	In frequency domain
Guard Bands	Time guard bands	Frequency guard bands
Implementation	Digital technique	Analog technique (originally)
Crosstalk	Less susceptible	More susceptible
Synchronization	Required	Not required

Diagram:



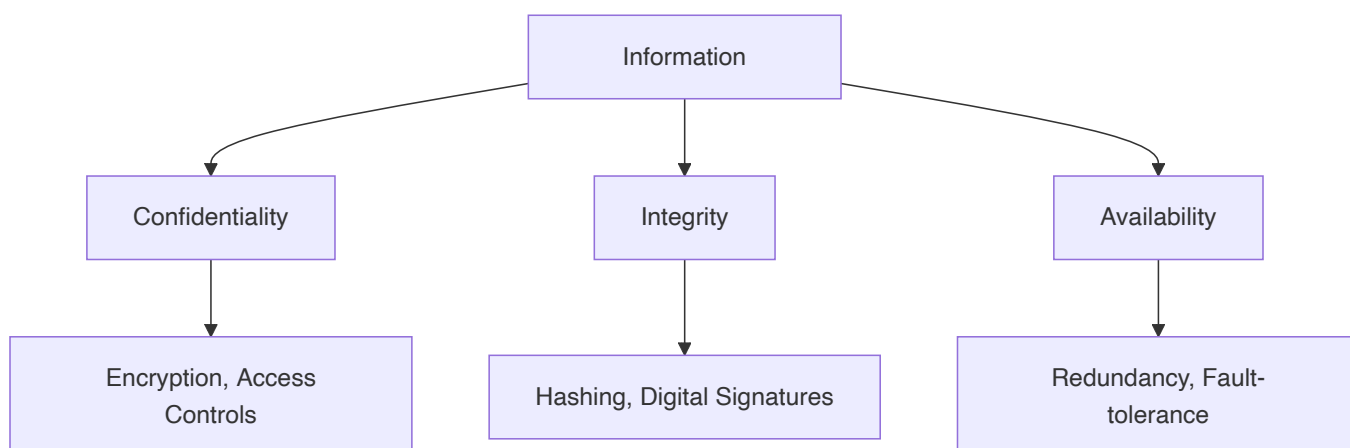
Mnemonic: "Time Divides Multiplexing, Frequency Divides Multiplexing"

Question 5(c) OR [7 marks]

Explain security components of information security.

Answer:

The CIA Triad of Information Security:



Component	Description	Implementation Methods
Confidentiality	Protection against unauthorized access	<ul style="list-style-type: none"> - Encryption - Access controls - Authentication - Steganography
Integrity	Ensuring data is accurate and unaltered	<ul style="list-style-type: none"> - Hashing - Digital signatures - Version control - Checksums
Availability	Ensuring systems are accessible when needed	<ul style="list-style-type: none"> - Redundancy - Backups - Disaster recovery - Fault tolerance
Authentication	Verifying identity	<ul style="list-style-type: none"> - Passwords - Biometrics - Smart cards - Multi-factor
Non-repudiation	Preventing denial of actions	<ul style="list-style-type: none"> - Digital signatures - Audit logs - Timestamps

Security Threats:

- Malware (viruses, worms, trojans)
- Social engineering
- Denial of Service (DoS)
- Man-in-the-middle attacks
- Insider threats

Mnemonic: "CIA Protects All Network Data"